

Title: IMAGING APPARATUS IN WHICH REDUCED RESOLUTION IMAGE DATA IS GENERATED, AND WHEN SUPPLIED OVER AN IMAGE DATA BUS, THE RESOLUTION IS INCREASED AND OUTPUTTED Serial No. 09/353,287

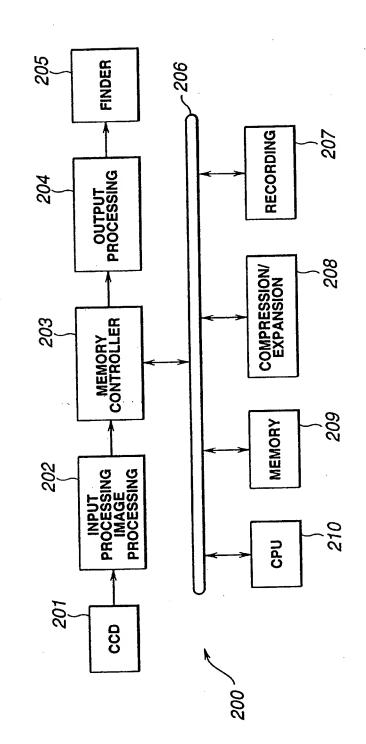


FIG.1 (PRIOR ART)



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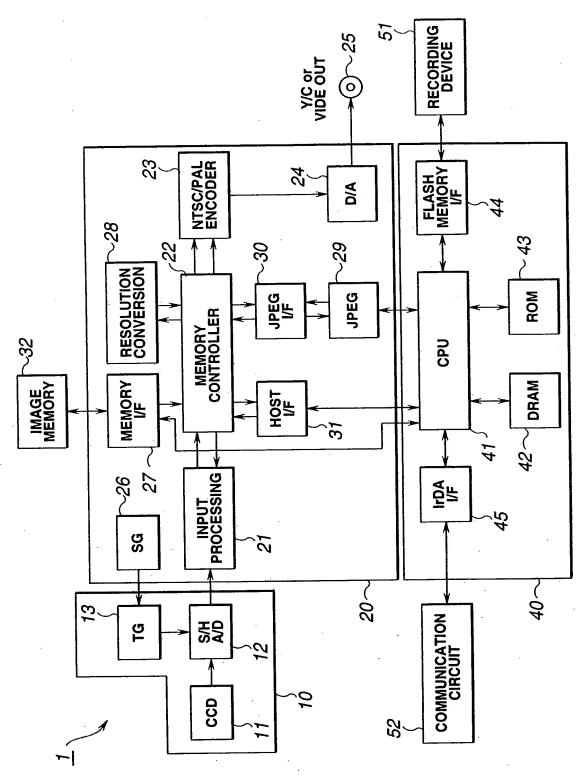


FIG.



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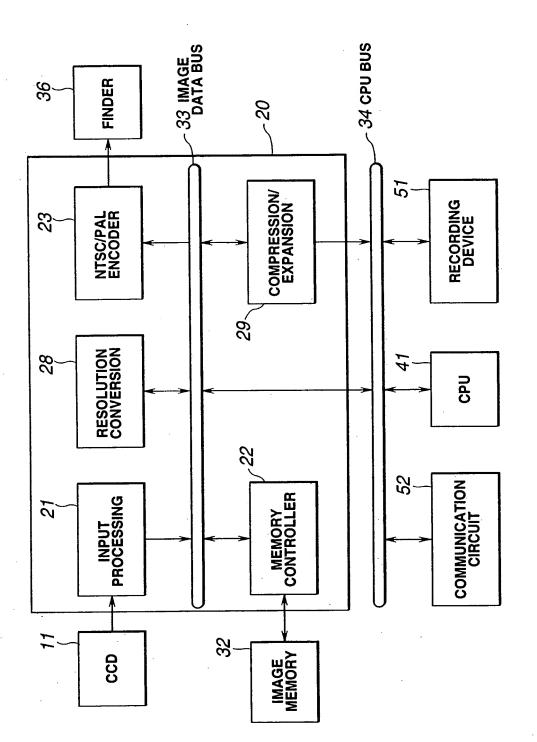


FIG.3



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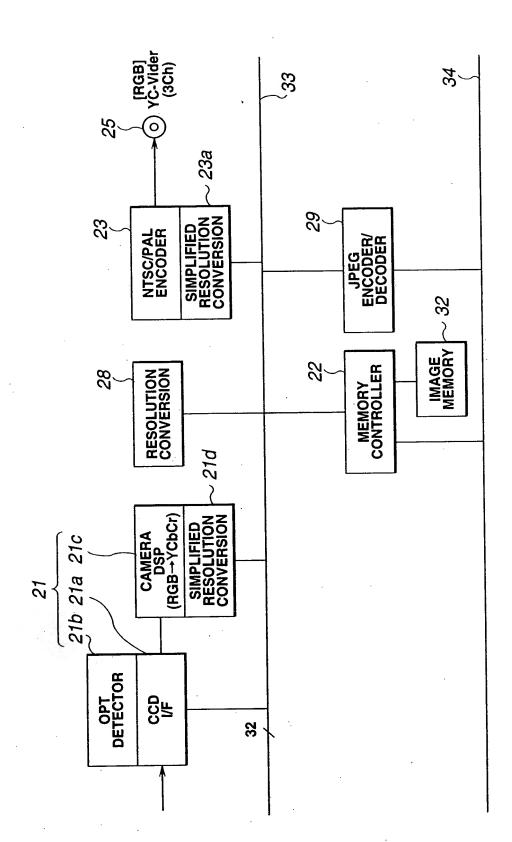


FIG.4



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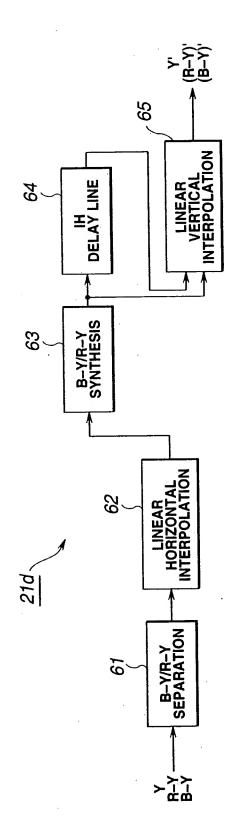
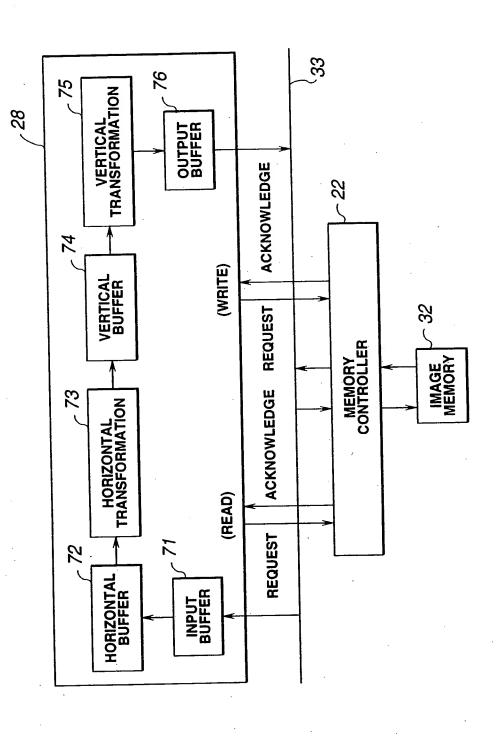


FIG.5



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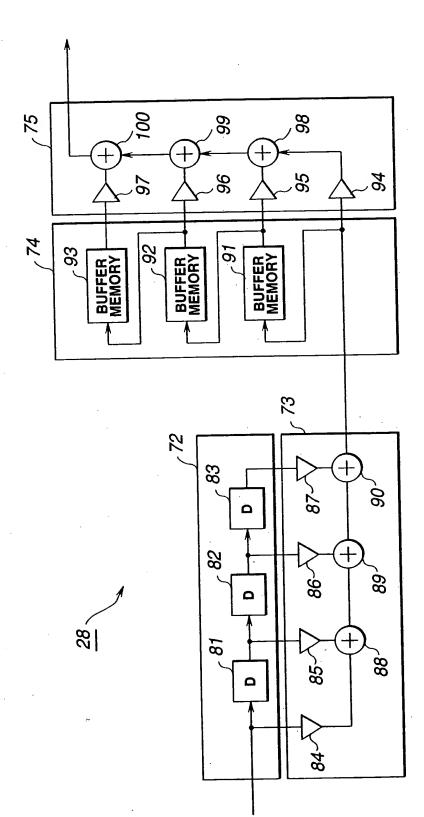
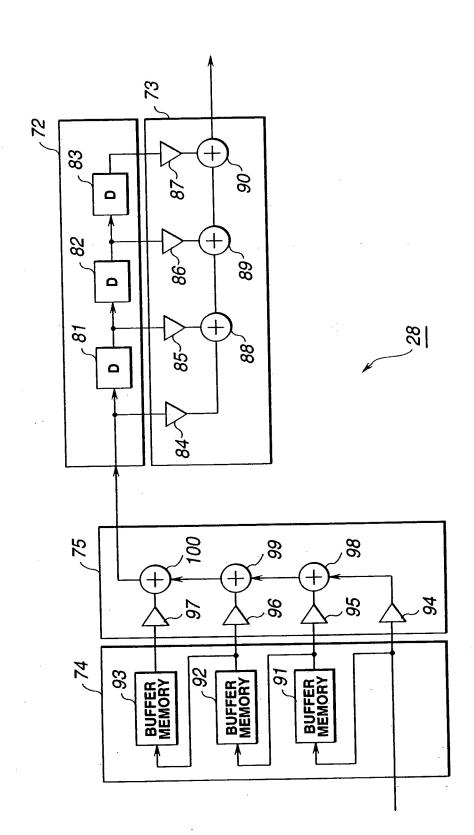


FIG.7



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五G.8



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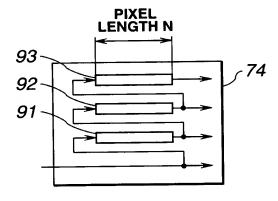


FIG.9

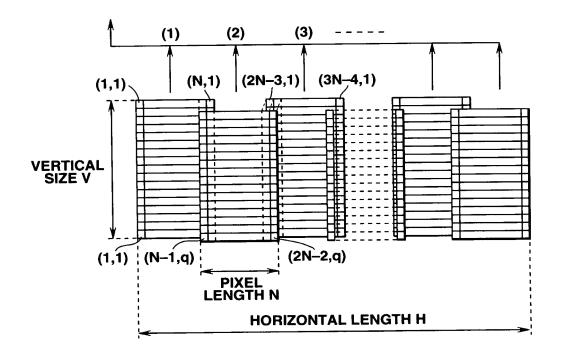


FIG.10



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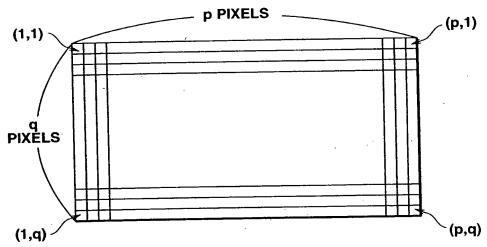


FIG.11

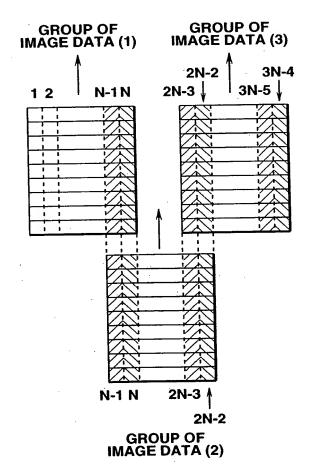


FIG.12



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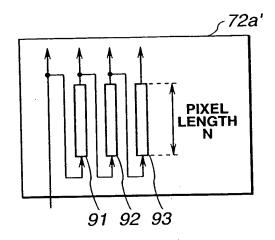


FIG.13

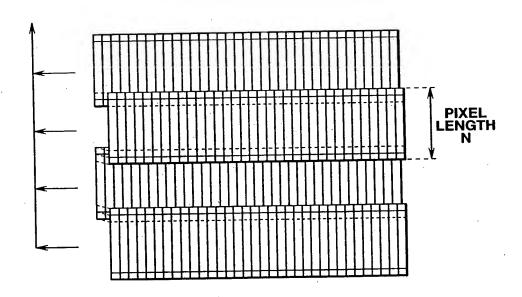


FIG.14

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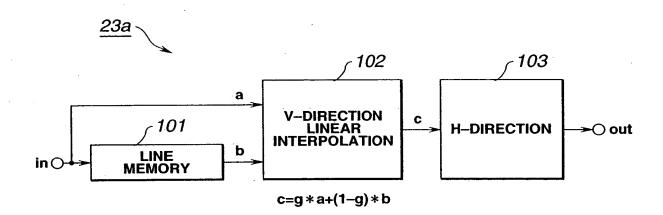


FIG.15

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FIG.16A 12.27MHz MWWWWWWWWWWWWWWWWWWWWWWWWWWWWWWWWWW

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cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr| က ဖ S က Ŋ ဖ FIG.16D DRAM→

0-0 Z 1 0 CAMDSP-DRAM C+D N+O DRAM → OSD DRAM→ NTSC N¹O Q+D FIG. 16F IBUS— (16bit)